

	Topic	Team Member	Date
1	New ARM SoC architecture	詹哲堯、賴奕翔	6/13
2	System level power estimation and low power design	林哲毅、陳宜武、錢宥達	6/13
3	System-level power estimation and low power design	吳岳達、鄭期文、林世傑	6/13
4	High-level synthesis	郭裔民、黃柏琛、王傑民	6/13
5	New EDA tools for ESL	陳毓茵、李鼎元、朱瑋晴	6/20
6	High-level power modeling, estimation, and optimization	陳奕、李懷霆、喬鈺安	6/20
7	GEZEL - Hardware/Software Codesign Environment	羅晟豪、鄞詩堯、歐岱鑫	6/20
8	Open Virtual Platforms	劉祐成、曾國維、彭則恩	6/20
9	System emulation with ALDEC HES-DVM	洪培恒、鍾孟庭、李岳穎	6/20