

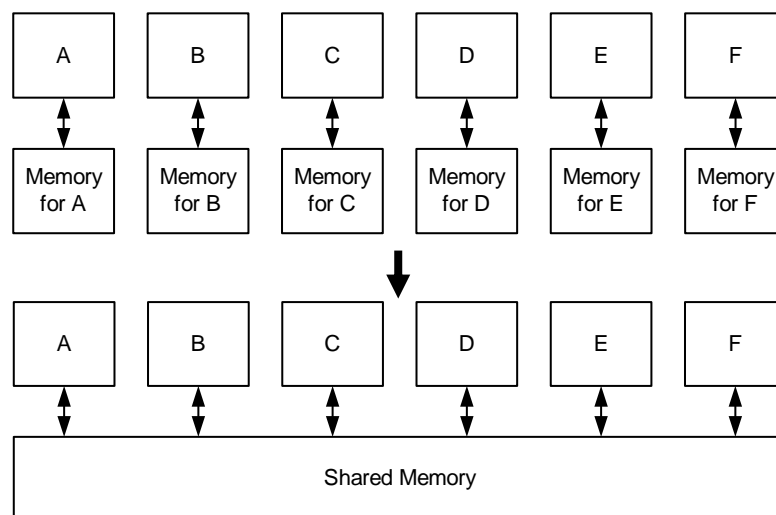
DSP in VLSI Design

Homework (VIII)

Scheduling and Resource Allocation

Deadline: Dec. 6

1. This problem addresses the relationship between scheduling and folding.
Scheduling can be used to derive the execution sequence (folding set) of an algorithm. Refer to the two scheduling shown in Slide 13, please derive the associated two folded architectures.
2. Consider a share-memory architecture. There are six modules, A, B, C, D, E, and F, in the system. And they use the same memory. Assume that this system can be used to handle three time-excluded tasks: the first task needs A, B, and D simultaneously; the second task needs A, B, C, and D simultaneously; the third task needs D, E, and F simultaneously. Note that when executing, each module require an independent memory bank, and we assume the required memory size for each module is the same. Please apply clique partition method to derive how many banks we need to implement such system.



Please deliver the homework to the TA:

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除非為程式作業，作業盡量繳交紙本格式